**UP TO DATE!**

**Inertial Module (2.5V PWR & 1.8V I/O)**

DONE

**GPS (3.3V PWR & I/O & 1.8V BACK-UP)**

DONE

**Microphones (1.8V PWR & I/O)**

DONE

**SDRAM (1.8V PWR & I/O)**

DONE

**MRAM (3.3V PWR &I/O)**

DONE

**SD Card and Voltage Level Shifter (3.3 PWR & 1.8V I/O)**

DONE

**FLASH (1.8V PWR & I/O)**

DONE

**CLOCK**

DONE

**POWER!**

-Pay attention to recommended layout when routing

**Solar Controller**

\*Picked resistor value for MPPC based on MP voltage of 2.19V from Solar cell data sheet.

\*BUCK resistance values taken from examples on device data sheet

\*Inductor chosen because it is the same Inductor series as our other inductors and we can swap out inductances easily. Data sheet was not very descriptive on

\*LDO tied directly to GND to get 2.2V as of not on pg11 of the data sheet

**Battery Monitor**

DONE

**CPLD**

DONE

**FPGA**

\*Decoupling caps taken from BeMicro design. VCC caps taken from PDN tool from Altera

-See connection guidelines for supply pins (Page 17-20)